

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kevin J. Ryan	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	303.539US3
Title:	MEMORY DEVICE INTERFACE		

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicant's prior U.S. application, Serial No. 10/238117, filed on September 10, 2002, which is relied upon for an earlier filing date under 35 U.S.C. §120.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

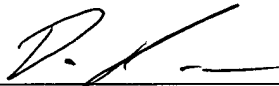
Respectfully submitted,

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Date of Deposit: February 27, 2004

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	Unknown
	Filing Date	Even Date Herewith
	First Named Inventor	Ryan, Kevin
	Group Art Unit	Unknown
	Examiner Name	Unknown
Sheet 1 of 1	Attorney Docket No: 303.539US3	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,748,635	05/05/1998	Thomann, M. , et al.	370	465	11/08/1996
	US-5,862,154	01/19/1999	Pawlowski, J. T.	371	40.11	01/03/1997
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	US-6,108,730	08/22/2000	Dell, T. J., et al.	710	102	02/27/1998
	US-6,449,679	09/10/2002	Ryan, Kevin	710	315	02/26/1999

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Intel Offers SDRAM Alternative to Rambus on Camino Chipsets", <u>Computergram International</u> , (July 14, 1999),1 p.	
		"SDRAM to Direct RDRAM", <u>Intel Development Forum Presentation</u> , Presentation Materials,(Undated),	
		CARROLL, M. , "Compaq, IBM, Others Seek Memory alternatives for Tanner- and Merced-class Systems -- Intel Preps Direct Rambus for Servers", <u>Electronic Engineering Times</u> , (Aug. 17, 1998),p. 1	
		HACHMAN, M. , et al., "Intel Simplifies Transition to New MPUs, Chipsets", <u>Electronic Buyers News</u> , (Dec. 7, 1998),p. 1	

EXAMINER

DATE CONSIDERED